In the Claims:

1. (Currently amended) A method of fabricating a semiconductor 1 2 device by employing ion implantation to SiC semiconductor substrate at a surface thereof with a 3 region having dopant introduced therein, comprising the 4 steps of: providing said semiconductor substrate at least 5 at a first region of a surface thereof with a mask layer including a polyimide resin film; and implanting dopant 8 ions.

Claims 2 to 5 (Canceled).

- 6. (Currently amended) The method of claim 1, wherein said semiconductor substrate is heated to at least 300°C and said dopant ions are implanted.
- 7. (Currently amended) The method of claim 1, wherein said
 2 semiconductor substrate is heated to at least 500°C and
 3 said dopant ions are implanted.
- 1 8. (Previously presented) The method of claim 1, wherein said
 2 polyimide resin film is formed of photosensitive polyimide
 3 resin.
- 9. (Currently amended) The method of claim 1, wherein said
 polyimide resin film has a thickness of at least twice a
 depth of implantation of said dopant introduced

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- ions implanted into said semiconductor substrate at a

 second region of said surface of said semiconductor

 substrate free of said polyimide resin film.
- 1 10. (Currently amended) The method of claim 1, wherein a thin
 2 metal film is posed further interposed between said
 3 polyimide resin film and said semiconductor substrate.
- 1 11. (Currently amended) The method of claim 1, wherein a thin
 2 film formed of SiO₂ is posed further interposed between said
 3 polyimide resin film and said semiconductor substrate.

Claims 12 to 24 (Canceled).

- 25. (Currently amended) The method of claim 1, wherein said
 mask layer is deposited on said semiconductor substrate at
 [[a]] said first region to be undoped with said dopant
 ions.
- ¹ **26.** (Currently amended) The method of claim 1, wherein said dopant ions are implanted into a <u>second</u> region unmasked by said mask layer.
- 1 27. (New) A method of preparing a doped semiconductor substrate, comprising the steps:
 - a) providing a semiconductor substrate comprising SiC;
- b) providing a mask layer including a polyimide resinfilm on a first region of a surface of said substrate;

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- 6 c) heating said substrate to at least 300°C; and
- while said substrate is at least 300°C, implanting, by
 ion implantation, dopant ions into said substrate
 through a second region of said surface to form in
 said substrate a doped region that is doped with said
 dopant ions.
- 1 28. (New) The method according to claim 27,

wherein said polyimide resin film consists of a photosensitive polyimide resin,

wherein said step b) comprises applying said polyimide resin film on said first region and said second region of said surface, then exposing said polyimide resin film to light at said first region, and then removing said polyimide resin film at said second region,

wherein said step b) does not involve photolithography employing a photoresist, and

wherein said method does not involve chemical vapor deposition and does not involve dry etching.

- 1 29. (New) The method according to claim 27, further comprising,
 2 after said step d), a step of removing said polyimide resin
 3 film by wet etching using hydrofluoric acid.
- 1 30. (New) The method according to claim 27, wherein said polyimide resin film has a thickness of at least twice a depth of said doped region.

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- 1 31. (New) The method according to claim 27, wherein said
 2 step b) further comprises forming said mask layer to
 3 include a metal film interposed between said polyimide
 4 resin film and said substrate.
- 1 32. (New) The method according to claim 27, wherein said 2 step b) further comprises forming said mask layer to 3 include a SiO₂ film interposed between said polyimide resin 4 film and said substrate.

[RESPONSE CONTINUES ON NEXT PAGE]